	Model Number 422E12						
	Overrange Low Frequency Respons High Frequency Respon High Frequency Respon High Frequency Respon Non-Linearity Environmental Temperature Range(Op	ensitivity(± 2 %)(Charge Conversion) everrange ow Frequency Response(-5 %) igh Frequency Response(2.2 mA) igh Frequency Response(4 mA) igh Frequency Response(20 mA) on-Linearity nvironmental emperature Range(Operating) emperature Response(Sensitivity Deviation) laximum Shock		SI 10 mV/pC ± 3 V 5 Hz 50 kHz 75 kHz 100 kHz ≤ 1.0 % FS -54 to +121 °C <1 % 9810 m/s² pk	[3] [3] [3]	Optional version TLD - TEDS (Temperature R Output Bias Vo	except v Capable of the capable of t
Electrical Excitation Voltage Output Bias Voltage Output Voltage(at specified measurement range) Constant Current Excitation Output Impedance Output Polarity Maximum Input Voltage Broadband Electrical Noise(1 to 10,000 Hz) Spectral Noise(1 Hz) Spectral Noise(10 Hz) Spectral Noise(10 Hz) Spectral Noise(10 KHz) Spectral Noise(10 kHz) Spectral Noise(10 kHz) Discharge Time Constant Resistance(Minimum required at input) Source Capacitance Loading		18 to 28 VDC 12.75 to 14.25 VDC ± 2.5 Vpk 2.2 to 20 mA <20 Ohm Inverted 30 V 20 µV 17.0 µV/√Hz 1.8 µV/√Hz 0.2 µV/√Hz 0.07 µV/√Hz 0.06 µV/√Hz 0.1 sec 7,000,000 Ohm 0.0005 %/pF	18 to 28 VDC 12.75 to 14.25 VDC ± 2.5 Vpk 2.2 to 20 mA <20 Ohm Inverted 30 V -94 dB -95 dB -115 dB -134 dB -144 dB 0.1 sec 7,000,000 Ohm 0.0005 %/pF	[1] [1] [1] [1] [1] [1]	NOTES: [1]Tested using variety charge output [2]Not to be used temperatures bias problems [3]Above stated to [4]See PCB Dec	sensor. d with low or contar). frequence	
	Physical Housing Material Sealing Electrical Connector(Inp Electrical Connector(Our Size (Diameter x Length Weight	tput)	Stainless Steel Welded 10-32 Coaxial Jack BNC Jack 0.52 in x 3.4 in 1.15 oz	Stainless Steel Welded 10-32 Coaxial Jack BNC Jack 13 mm x 86 mm 32.7 gm		Entered: LK	Engine

OPTIONAL VERSIONS

Optional versions have identical specifications and accessories as listed for the standard model except where noted below. More than one option may be used.

TLD - TEDS Capable of Digital Memory and Communication Compliant with IEEE 1451.4 Temperature Range(Operating) -40 to +185 °F -40 to +85 °C Output Bias Voltage 13.35 to 14.85 VDC 13.35 to 14.85 VDC

NOTES:

- [1]Tested using voltage source and input capacitor equal to the feedback capacitor, to simulate a charge output sensor.
- [2]Not to be used with low values of source resistance such as charge mode sensors at elevated temperatures or contaminated sensor cables (preventing low frequency peaking and/or output

[3]Above stated frequency, the amplifier becomes slew rate limited.
[4]See PCB Declaration of Conformance PS024 for details.

Entered: LK	Engineer: CPH	Sales: ML	Approved: DY	Spec Number:
Date: 8/10/2016	Date: 8/10/2016	Date: 8/10/2016	Date: 8/10/2016	422-5120-80



Phone: 716-684-0001 Fax: 716-684-0987 E-Mail: info@pcb.com

Revision: R

ECN #: 45760



NA - del NI - ede

All specifications are at room temperature unless otherwise specified.

In the interest of constant product improvement, we reserve the right to change specifications without notice.

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